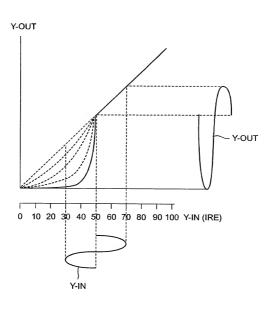
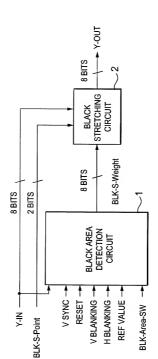
FIG.1 PRIOR ART







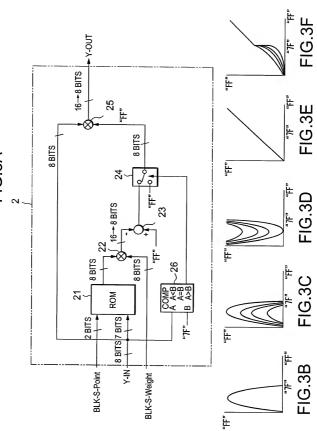
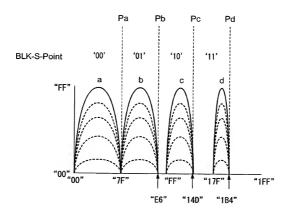


FIG.3A

FIG.4



BLK-S-Weight 8 BITS BIT WIDTH CONVERTER COUNTF1D 18 BITS, DECISION SIGNAL COUNTER 2 BITS DLSTHR VP1D VP2D VALUE COMPARATOR GENERATOR REFERENCE VERTICAL PULSE V SYNC -RESET-V BLANKING -H BLANKING -. N-≻ REF VALUE BLK-Area-SW

FIG.6

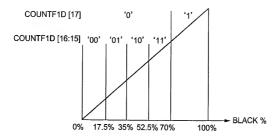


FIG.7

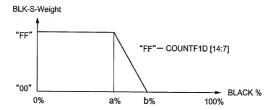


FIG.8

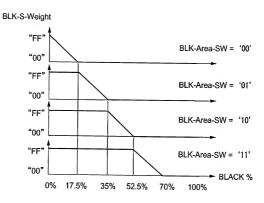
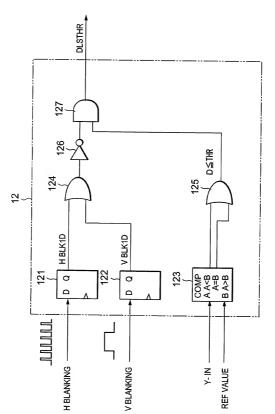


FIG.

	_		Т	Τ	Γ	
		.44	00,,	00,,	,,00,,	"FF"- COUNTF1D [14:7]
	1	.10.	00,,	00,,	"FF" - COUNTF1D [14:7]	"ŁŁ"
	,	.01	"00"	"FF" - COUNTF1D [14:7]	"FF"	"FF"
	,000	00	"FF"- COUNTF1D [14:7]	"FF"	"FF"	"FF"
	COUNTF1D [16:15]	BLK-Area-SW [1:0]	,00,	,01,	,10,	'11'

D Q VSYNC1D D Q VSYNC2D

FIG.11



COUNTF1D



132 133 134 CLR Q COUNTF D Q

131

VP2D — RESET — DLSTHR —

VP1D

FIG.13

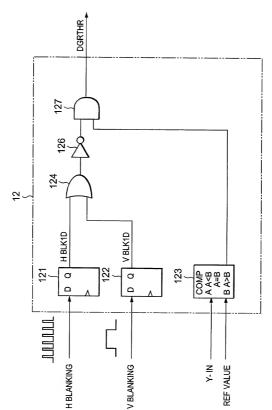
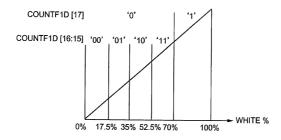


FIG.14



The street of th

FIG.15

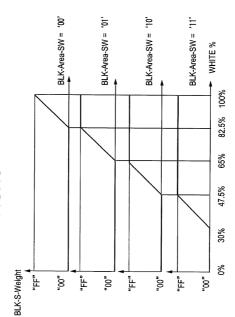


FIG.16

	-				
,11,		"FF"	"FF"	"FF"	COUNTF1D [14:7]
,10,		"±±"	"ŁŁ"	COUNTF1D [14:7]	"00"
,01,		"FF"	COUNTF1D [14:7]	"00"	"00"
,00,		COUNTF1D [14:7]	00,,	"00"	00,,
COUNTF1D [16:15]	BLK-Area-SW [1:0]	,00,	,01,	,10,	,11,